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FORM PTO-1449

U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO.

108339-00031

SERIAL NO.

09/08/1998

APPLICANT

Anders JOHNSON

FILING DATE

October 11, 2000

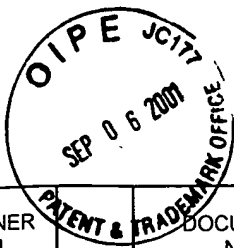
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## LIST OF REFERENCES CITED BY APPLICANT

(Use several sheets if necessary)

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	AD	5,828,653	Oct. 27, 1998	Goss			Apr. 26, 1996
	AE	5,423,015	Jun. 6, 1995	Chung			May 21, 1991
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	AG	6,061,351	May 9, 2000	Erimli et al.			Dec. 18, 1997
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	CJ	"Local Area Network Switch Frame Lookup Technique for Increased Speed and Flexibility," IBM Technical Disclosure Bulletin 38(1995) July, No. 7, Armonk, NY, US, pages 221-222
	CK	"Queue Management for Shared Buffer and Shared Multi-buffer ATM Switches," Yu-Sheng Lin et al. Department of Electronics Engineering & Institute of Electronics, National Chiao Tung University, Hsinchu, Taiwan, R.O.C., March 24, 1996, pages 688-695
	CL	"A 622-Mb/s 8 x 8 ATM Switch Chip Set with Shared Multibuffer Architecture," Harufusa Kondoh et al., 8107 IEEE Journal of Solid-State Circuits 28(1993) July, No. 7, New York, US, pages 808-814
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Sheet 1 of 1

FORM PTO-1449  <b>LIST OF REFERENCES CITED BY APPLICANT</b>  (Use several sheets if necessary)	ATTY. DOCKET NO. 108339-00031	SERIAL NO. 09/685,192
	APPLICANT JOHNSON	
	FILING DATE October 11, 2000	GROUP 2131

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NO.	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
LAH	AA	5,499,295	03/12/96	Cooper			
LAH	AB	5,652,579	07/29/97	Yamada et al			
	AC						
	AD						
	AE						
	AF						

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## FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION YES NO PART.		
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	AH								
	AI								
	AJ								
	AK								
	AL								

## OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

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